

TSSOP-8 P 1. 8 2

Pin Definition:

 1. Drain 1
 8. Drain 2

 2. Source 1
 7. Source 2

 3. Source 1
 6. Source 2

 4. Gate 1
 5. Gate 2

PRODUCT SUMMARY

V _{DS} (V)	R _{DS(on)} (mΩ)	I _D (A)
-20	40 @ V _{GS} = -4.5V	-5
	50 @ V _{GS} = -2.5V	-4
	60 @ V _{GS} = -1.8V	-3

Features

- Advance Trench Process Technology
- High Density Cell Design for Ultra Low On-resistance

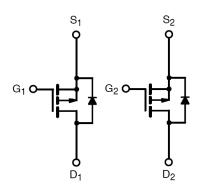
Application

- Load Switch
- PA Switch

Ordering Information

Part No.	Package	Packing
TSM6981DCA RF	TSSOP-8	T&R

Block Diagram



Dual P-Channel MOSFET

Absolute Maximum Rating (Ta = 25 °C unless otherwise noted)

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	-20	V	
Gate-Source Voltage		V _{GS}	±8	V	
Continuous Drain Current, V _{GS} @4.5V	·	I _D	-5	А	
Pulsed Drain Current, V _{GS} @4.5V		I _{DM}	-30	А	
Continuous Source Current (Diode Co	onduction) ^{a,b}	I _S	-1.0	А	
	Ta = 25 °C		1.14	W	
Maximum Power Dissipation	Ta = 70 °C	PD	0.73		
Operating Junction Temperature		TJ	+150	°C	
Operating Junction and Storage Temp	erature Range	T _J , T _{STG}	T _J , T _{STG} - 55 to +150		

Thermal Performance

Parameter	Symbol	Limit	Unit
Junction to Foot (Drain) Thermal Resistance	Rθ _{JF}	40	°C/W
Junction to Ambient Thermal Resistance (PCB mounted)	RƏ _{JA}	75	°C/W

Notes:

a. Surface Mounted on 1" x 1" FR4 Board.

b. Pulse width limited by maximum junction temperature



Pb

TSM6981D 20V Dual P-Channel MOSFET

Electrical Specifications

oHS

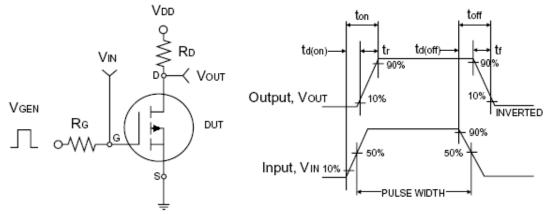
Parameter	Conditions	Symbol	Min	Тур	Max	Unit
Static						
Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = - 250uA	BV _{DSS}	-20			V
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = -250 \text{uA}$	V _{GS(TH)}	-0.4		-1.0	V
Zero Gate Voltage Drain Current	V _{DS} = -16V, V _{GS} = 0V	I _{DSS}			-1.0	uA
Gate Body Leakage	V_{GS} = ±8V, V_{DS} = 0V	I _{GSS}			±100	nA
On-State Drain Current	V _{DS} ≤-5V, V _{GS} = -4.5V	I _{D(ON)}	-20			Α
	V_{GS} = -4.5V, I_{D} = -5A			30	30	
Drain-Source On-State Resistance	V_{GS} = -2.5V, I_{D} = -4A	R _{DS(ON)}		40	50	mΩ
	$V_{GS} = -1.8V, I_D = -3A$		50	60		
Forward Transconductance	V _{DS} = - 5V, I _D = - 5A	g _{fs}		17		S
Diode Forward Voltage	I _S = - 1.0A, V _{GS} = 0V	V _{SD}		- 0.6	-1.2	V
Dynamic ^b			-	-		-
Total Gate Charge	-1/-10//-170	Qg		12.5	19	
Gate-Source Charge	$\begin{split} & V_{DS} = V_{GS}, \ I_D = -\ 250 uA \\ & V_{DS} = -16V, \ V_{GS} = 0V \\ & V_{GS} = \pm 8V, \ V_{DS} = 0V \\ & V_{DS} \leq -5V, \ V_{GS} = -4.5V \\ & V_{GS} = -4.5V, \ I_D = -5A \\ & V_{GS} = -2.5V, \ I_D = -4A \\ & V_{GS} = -1.8V, \ I_D = -3A \\ & V_{DS} = -5V, \ I_D = -5A \end{split}$	Q _{gs}		1.7		nC
Gate-Drain Charge	V _{GS} 4.5V	Q _{gd}		3.3		
Input Capacitance		C _{iss}		1020		
Output Capacitance		C _{oss}		191		pF
Reverse Transfer Capacitance		C _{rss}		140		
Switching ^c						
Turn-On Delay Time	$-I_{\rm D}$ = -1A, V _{GEN} = -4.5V,	t _{d(on)}		25	40	
Turn-On Rise Time		tr		43	65	
Turn-Off Delay Time		t _{d(off)}		71	110	nS
Turn-Off Fall Time	ng - 022	t _f		48	75	

Notes:

a. pulse test: PW ≤300µS, duty cycle ≤2%

b. For DESIGN AID ONLY, not subject to production testing.

b. Switching time is essentially independent of operating temperature.

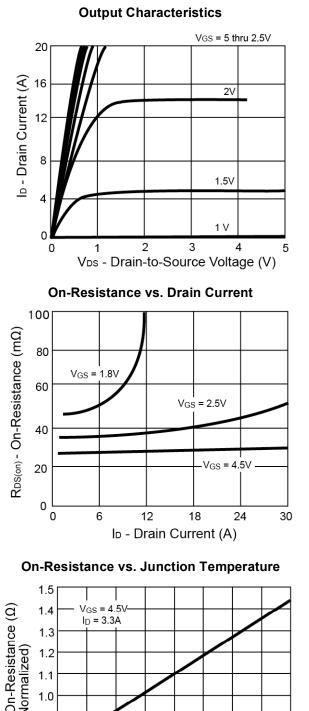


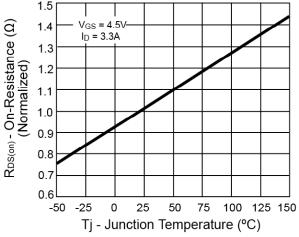
Switching Test Circuit

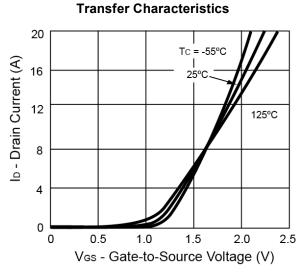
Switchin Waveforms



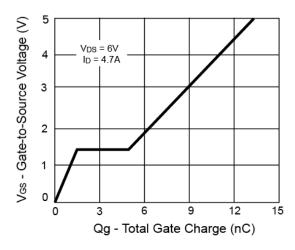
Electrical Characteristics Curve (Ta = 25 °C, unless otherwise noted)



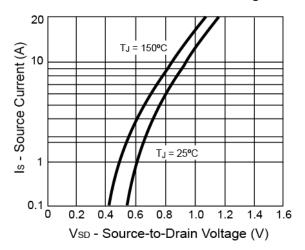




Gate Charge

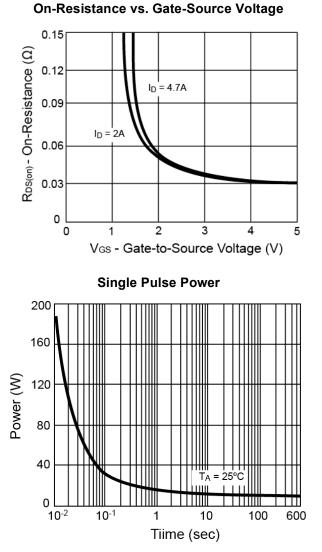


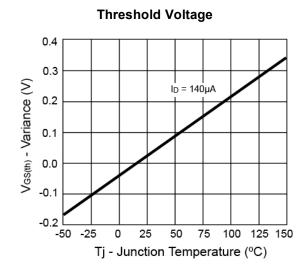
Source-Drain Diode Forward Voltage



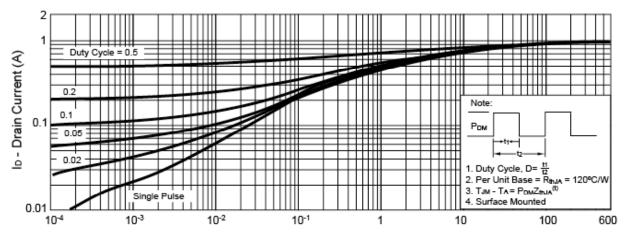


Electrical Characteristics Curve (Ta = 25 °C, unless otherwise noted)





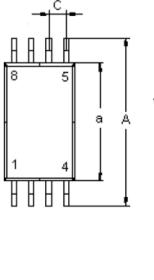
Normalized Thermal Transient Impedance, Junction-to-Ambient



Square Wave Pulse Duration (sec)



TSSOP-8 Mechanical Drawing

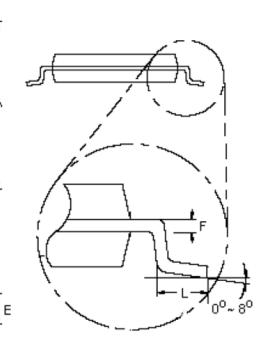


ķ

e

Β

D



TSSOP-8 DIMENSION					
5.14	MILLIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
А	6.20	6.60	0.244	0.260	
а	4.30	4.50	0.170	0.177	
В	2.90	3.10	0.114	0.122	
С	0.65 (typ)		0.025 (typ)		
D	0.25	0.30	0.010	0.019	
ш	1.05	1.20	0.041	0.049	
e	0.05	0.15	0.002	0.009	
F	0.127		0.005		
L	0.50	0.70	0.020	0.028	



Notice

Specifications of the products displayed herein are subject to change without notice. TSC or anyone on its behalf, assumes no responsibility or liability for any errors or inaccuracies.

Information contained herein is intended to provide a product description only. No license, express or implied, to any intellectual property rights is granted by this document. Except as provided in TSC's terms and conditions of sale for such products, TSC assumes no liability whatsoever, and disclaims any express or implied warranty, relating to sale and/or use of TSC products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright, or other intellectual property right.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications. Customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify TSC for any damages resulting from such improper use or sale.